

15. CMC-Q9-HCM - Task #1197

Thực hiện thay thế RAM host 29 P2-CD

22-09-2024 18:49 - Hiếu Nguyễn Phùng

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|-----------------------|--------------------|-----------------------------|------------|
| Trạng thái: | Closed | Bắt đầu: | 22-09-2024 |
| Mức ưu tiên: | Normal | Hết hạn: | |
| Phân công cho: | Hiếu Nguyễn Phùng | Tiến độ: | 100% |
| Chủ đề: | | Thời gian ước lượng: | 0:00 giờ |
| Phiên bản: | 14/10 - 20/10/2024 | Thời gian: | 0:00 giờ |

Mô tả

Host 29 P2-CD hiện đang gặp tình trạng treo host

| Severity | Date/Time | Description |
|----------|---------------------|--|
| ✓ | 2024-09-11 14:15:27 | SEL Fullness: Event Logging Disabled sensor, Log Area Reset/Cleared was asserted |
| ✓ | 2024-09-11 14:15:56 | System Event, System Event,POST starts with BIOS 2.5.6 |
| ✓ | 2024-09-11 14:16:29 | System Event, POST ends with PC Compatible Boot(Legacy),USB Storage Boot |
| ✓ | 2024-09-13 04:50:08 | System Event, System Event,POST starts with BIOS 2.5.6 |
| ✓ | 2024-09-13 04:50:39 | System Event, POST ends with PC Compatible Boot(Legacy),USB Storage Boot |
| ✗ | 2024-09-13 08:55:00 | Memory: Memory Sensor sensor, Uncorrectable ECC Error, Unspecified, CPU2 DIMM_B3 |
| ✓ | 2024-09-13 08:55:00 | Sys Pwr Monitor: System ACPI Power State sensor, S5 / G2 "soft-off" was asserted |
| ✓ | 2024-09-13 08:55:06 | Sys Pwr Monitor: System ACPI Power State sensor, S0 / G0 "working" was asserted |
| ✓ | 2024-09-13 08:55:22 | System Event, System Event,POST starts with BIOS 2.5.6 |
| ✓ | 2024-09-13 08:55:53 | System Event, POST ends with PC Compatible Boot(Legacy),USB Storage Boot |
| ✗ | 2024-09-13 09:19:27 | Memory: Memory Sensor sensor, Uncorrectable ECC Error, Unspecified, CPU2 DIMM_B3 |
| ✗ | 2024-09-13 09:19:27 | CPU2_CAT_ERR: Processor sensor, IERR was asserted |
| ✓ | 2024-09-13 09:19:27 | Sys Pwr Monitor: System ACPI Power State sensor, S5 / G2 "soft-off" was asserted |
| ✓ | 2024-09-13 09:19:34 | Sys Pwr Monitor: System ACPI Power State sensor, S0 / G0 "working" was asserted |
| ✓ | 2024-09-13 09:19:49 | System Event, System Event,POST starts with BIOS 2.5.6 |
| ✓ | 2024-09-13 09:20:20 | System Event, POST ends with PC Compatible Boot(Legacy),USB Storage Boot |
| ✗ | 2024-09-13 09:22:20 | Memory: Memory Sensor sensor, Uncorrectable ECC Error, Unspecified, CPU2 DIMM_B3 |
| ✗ | 2024-09-13 09:22:20 | CPU2_CAT_ERR: Processor sensor, IERR was asserted |
| ✓ | 2024-09-13 09:22:20 | Sys Pwr Monitor: System ACPI Power State sensor, S5 / G2 "soft-off" was asserted |
| ✓ | 2024-09-13 09:22:27 | Sys Pwr Monitor: System ACPI Power State sensor, S0 / G0 "working" was asserted |
| ✓ | 2024-09-13 09:22:42 | System Event, System Event,POST starts with BIOS 2.5.6 |
| ✓ | 2024-09-13 09:23:13 | System Event, POST ends with PC Compatible Boot(Legacy),USB Storage Boot |
| ✗ | 2024-09-13 09:26:16 | Memory: Memory Sensor sensor, Uncorrectable ECC Error, Unspecified, CPU2 DIMM_B3 |
| ✗ | 2024-09-13 09:26:16 | CPU2_CAT_ERR: Processor sensor, IERR was asserted |
| ✓ | 2024-09-13 09:26:16 | Sys Pwr Monitor: System ACPI Power State sensor, S5 / G2 "soft-off" was asserted |
| ✓ | 2024-09-13 09:26:22 | Sys Pwr Monitor: System ACPI Power State sensor, S0 / G0 "working" was asserted |
| ✓ | 2024-09-13 09:26:37 | System Event, System Event,POST starts with BIOS 2.5.6 |
| ✓ | 2024-09-13 09:27:08 | System Event, POST ends with PC Compatible Boot(Legacy),USB Storage Boot |
| ✗ | 2024-09-13 09:28:47 | Memory: Memory Sensor sensor, Uncorrectable ECC Error, Unspecified, CPU2 DIMM_B3 |
| ✓ | 2024-09-13 09:28:47 | Sys Pwr Monitor: System ACPI Power State sensor, S5 / G2 "soft-off" was asserted |
| ✓ | 2024-09-13 09:28:53 | Sys Pwr Monitor: System ACPI Power State sensor, S0 / G0 "working" was asserted |
| ✓ | 2024-09-13 09:29:11 | System Event, System Event,POST starts with BIOS 2.5.6 |
| ✗ | 2024-09-13 09:29:42 | Memory: Memory Sensor sensor, Memory Device Disabled, Unspecified, CPU2 DIMM_B3 |
| ✗ | 2024-09-13 09:29:42 | Memory: Memory Sensor sensor, Memory Device Disabled, Unspecified, CPU2 DIMM_B7 |
| ✗ | 2024-09-13 09:29:42 | Svstem Firmware Proaress: POST error code: 8100h |

Check log báo lỗi RAM

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Tiến hành chuẩn bị thay thế RAM cho host

1. Chuẩn bị sẵn file backup config, usb boot.
2. OFF node
3. Thay thế RAM bị lỗi
4. ON lại node
5. Để node ở trạng thái maintenance
6. kiểm tra lại log xem còn báo lỗi không
7. Tạm thời không để VM trên node để theo dõi thêm

Lược sử

#1 - 22-09-2024 19:22 - Hiếu Nguyễn Phùng

- Chủ đề thay đổi từ Thực hiện thay thế RAM host 29 tới Thực hiện thay thế RAM host 29 P2-CD

#2 - 25-09-2024 09:33 - Hiếu Nguyễn Phùng

- Mô tả cập nhật

#3 - 10-10-2024 09:57 - Hiếu Nguyễn Phùng

- Mô tả cập nhật

#4 - 20-10-2024 20:30 - Hiếu Nguyễn Phùng

- Trạng thái thay đổi từ In Progress tới Closed

- Phiên bản gán cho 14/10 - 20/10/2024

- Tiến độ thay đổi từ 0 tới 100

Tập tin

| | | | |
|----------------------------------|--------|------------|-------------------|
| clipboard-202409221847-jpy5j.png | 115 KB | 22-09-2024 | Hiếu Nguyễn Phùng |
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